

PATENT

IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Douglass
Assignee: Xilinx, Inc.
Title: "Method of Designing Integrated Circuit Having Both Configurable and Fixed Logic Circuitry"

Serial No.: 10/092,051 File Date: 03-04-02
Examiner: Unknown Art Unit: 2811
Docket No.: X-923 US

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

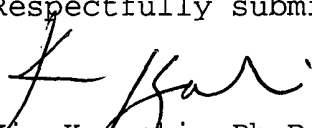
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the one (1) reference listed in the attached Substitute for Form 1449A/PTO. A copy of each is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above document shall not be construed as an admission that the document is necessarily prior art with respect to the instant invention. Citation of the above document shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above document shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,


Kim Kanzaki, Ph.D.
Attorney for Applicant
Reg. No. 37,652

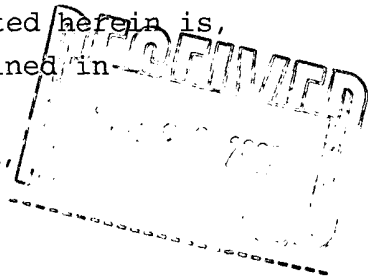
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First Named Invent r	Stephen M. Douglass
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Attorney Docket Number	X-923 US

[illegible][illegible]Date
Considered

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